	Type	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	231	702/122.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:02	
2	BRS	5764	326/21,26-28,30,31,35,82,83,86.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:03	
3	BRS	3373	327/108-110,379,386,387.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:04	
4	BRS	2	"20040267469"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:05	
5	BRS	0	(integrated adj circuit$1 or IC$1) and receiv$3 with signal$1 with port$1 with captur$3 with waveform$1 with simultaneous	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:09	
6	BRS	1	(integrated adj circuit$1 or IC$1) and receiv$3 with signal$1 with port$1 with captur$3 with waveform$1 and simultaneous with bidirectional with node$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:10	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
7	BRS	1	(integrated adj circuit$1 or IC$1) and receiv$3 with signal$1 with port$1 with captur$3 with waveform$1 and bidirectional with node$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:10	
8	BRS	1	(integrated adj circuit$1 or IC$1) and receiv$3 with signal$1 with port$1 with waveform$1 and simultaneous with bidirectional with node$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:10	
9	BRS	6	(integrated adj circuit$1 or IC$1) and receiv$3 with signal$1 with port$1 with waveform$1 and bidirectional with node$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:11	
10	BRS	6	(integrated adj circuit$1 or IC$1 or wafer$1 or die$1 or chip$1 or semiconductor$1) and receiv$3 with signal$1 with port$1 with waveform$1 and bidirectional with node$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/13 11:22	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
11	BRS	0	(integrated adj circuit$1 or IC$1 or wafer$1 or die$1 or chip$1 or semiconductor$1) and receiv$3 with signal$1 and port$1 with receiv$3 with digit$1 with first with operat$3 and waveform$1 with second with operat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:14	
12	BRS	3	(integrated adj circuit$1 or IC$1 or wafer$1 or die$1 or chip$1 or semiconductor$1) and receiv$3 with signal$1 and port$1 with receiv$3 with digit$2 with first with operat$3 and waveform$1 with second with operat$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:25	
13	BRS	26283	(integrated adj circuit$1 or IC$1 or wafer$1 or die$1 or chip$1 or semiconductor$1) and port with circuit$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:25	
14	BRS	17170	(integrated adj circuit$1 or IC$1) and port with circuit$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/04/12 15:26	

Type	Hits	Search Text	DBs	Time Stamp	Comments
15	BRS	614	(integrated adj circuit$1 or IC$1) and port with circuit$1 with receiver$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:26	
16	BRS	267	(integrated adj circuit$1 or IC$1) and port with circuit$1 with receiver$1 with signal$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:26	
17	BRS	69	(integrated adj circuit$1 or IC$1) and port with circuit$1 with receiver$1 with receiving with signal$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:26	
18	BRS	0	(integrated adj circuit$1 or IC$1) and port with circuit$1 with receiver$1 with receiving with signal$1 with waveform$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:27	
19	BRS	7	(integrated adj circuit$1 or IC$1) and port with circuit$1 with receiver$1 with signal$1 with waveform$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 15:29	

Type	Hits	Search Text	DBs	Time Stamp	Comments
20 BRS	9	(integrated adj circuit$1 or IC$1) and count$3 with circuit$1 with receiver$1 with signal$1 with waveform$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 16:25	
21 BRS	119	(integrated adj circuit$1 or IC$1) and port with circuit$1 with receiv$3 with compar$4 with output$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 16:26	
22 BRS	0	(integrated adj circuit$1 or IC$1) and port with circuit$1 with receiv$3 with compar$4 with output$3 with sampl$3 with clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 16:26	
23 BRS	3	(integrated adj circuit$1 or IC$1) and port with circuit$1 with receiv$3 with compar$4 with output$3 with sampl$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/12 16:26	
24 IS&R	2	("6621323").PN.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/13 11:14	

Type	Hits	Search Text	DBs	Time Stamp	Comments
BRS	18	25 (integrated adj circuit$1 or IC$1 or wafer$1 or die$1 or chip$1 or semiconductor$1) and receiv$3 with signal$1 with port$1 with waveform$1 and (bi-direction$2 or bidirection$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/04/13 11:22	